

Building a RISC-V CPU Core (LFD111x)

Create a RISC-V CPU with modern open source circuit design tools, methodologies, and microarchitecture, all from your browser.

Duration: 1 Day

Prerequisites for this course

- Anyone with a technical inclination can successfully complete the workshop. No prior knowledge of digital logic design is required.

Outline for this course

Chapter 1 – Learning Platform

Chapter 2 – Digital Logic

Chapter 3 – The Role of RISC-V

Chapter 4 – RISC-V-Subset CPU

Chapter 5 – Completing Your RISC-V CPU